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				Application Number	10/707,926
				Filing Date	January 4, 2002
				First Named Inventor	Gobinda Das
				Art Unit	To Be Assigned
				Examiner Name	To Be Assigned
				Attorney Docket Number	21808-00146-US1
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number Number/Kind Code* (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.†	Foreign Patent Document Country Code* - Number* - Kind Code* (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
012	BA	JP-10115654 A *	05-06-1998			

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T*
012	CA	W.H. McAnney, "Improving the Fault Coverage of Boundary Scan," Research Disclosure, March 1991, No. 323, England. *			
012	CB	"Contactless On-Chip AC I/O Wrap Test," Research Disclosure 41, September 1998, Kenneth Mason Publications Ltd., England. *			

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